



**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

Confirmation No.: 3133

SUZUKI et al.

Art Unit: 2811

Serial No.: 10/058,005

Examiner: J. Im

Filed: January 29, 2002

Docket No.: 100353-00096

For: SEMICONDUCTOR INTEGRATED CIRCUIT CONFIGURED TO SUPPLY  
SUFFICIENT INTERNAL CURRENT

**AMENDMENT UNDER 37 C.F.R. § 1.111**

**Introductory Comments**

Director of the U.S. PTO  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

June 24, 2004

Sir:

In response to the Office Action dated February 27, 2004, the period for response being extended from May 27, 2004 until June 27, 2004 by the attached Petition for Extension of Time, please amend the above-titled application as follows: